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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/724,440	11/26/2003	Debanjan Mukherjee	M-15289 US	1199

7590 05/05/2006

Jonathan W. Hallman MacPHERSON KWOK CHEN & HEID LLP 1762 Technology Drive, Suite 226 San Jose, CA 95110 BERHANE, ADOLF D

ART UNIT PAPER NUMBER

EXAMINER

2838

DATE MAILED: 05/05/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. Extensions of unitempt be available under the provision of 37 GF1 1306. In no event, however, may a reply be timely filled between the procession of 37 GF1 1306. In no event, however, may a reply be timely filled in the process of the proces		Application No.	Applicant(s)				
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Application/Control Number: 10/724,440 Page 2

Art Unit: 2838

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.
- 2. Claims 10-12 are rejected under 35 U.S.C. 102(a) as being anticipated by Jaussi et al. (6,501,256).

Jaussi et al. disclose a trimmable bandgap voltage reference in Figs. 1, 3-5 and 8-12. Bandgap voltage reference circuit (100), differential amplifier (130), current source transistors (140, 150, 160), output resistor (170), sources current (144, 154, 164), parallel combination circuit (110) includes resistor (112) in parallel with diode (114), parallel combination circuit (120) includes resistor (122) in parallel with a series combination of variable resistor (124) and diode (126), the series combination of variable resistor and diode have a current flowing there through shown as current (128), Fig. 3 shows a variable resistor for use as variable resistor (124) (Fig. 1) and variable resistor (300) includes multiple resistive device, includes resistive devices (302, 304, 306, 308 and 310).

Allowable Subject Matter

3. Claims 1-9 and 13-17 are allowed.

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Response to Arguments

4. Applicant's arguments filed 3/17/06 have been fully considered but they are not persuasive. The recitation that the first resistor such that a conductance of the first variable resistor equals a sum of a conductance for the first resistor and a combined conductance for the selected second resistors has not been given patentable weight because it has been that a preamble is denied the effect of a limitation where the claim is drawn to a structure and the portion of the claim following the preamble is a self-contained description of the structure not depending for completeness upon the introductory clause. *Kropa v. Robie*, 88 USPQ 478 (CCPA 1951).

Conclusion

5. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Adolf Berhane whose telephone number is 571-272-2077. The examiner can normally be reached on Monday- Friday 8 AM to 6 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Karl Easthom can be reached on 571-272-1989. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Adolf Bernane Primary Examiner Art Unit 2838